

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re the Application of:

MIN-HYUNG CHO, ET AL.

Application No.:

Filed:

For: **Quadrature Modulation Transmitter**

Art Group:

Examiner:

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.97

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In accordance with the duty of disclosure, enclosed is a copy of Information Disclosure Statement by Applicant (form PTO/SB/08), which is being submitted concurrently with the Utility Application. It is respectfully requested that the cited references be considered and that the enclosed copy of PTO/SB/08 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s).

The submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made in the subject application and is not to be construed as an admission that the information cited in this statement is material to patentability.

Please charge any fees due to Deposit Account 02-2666. A duplicate copy of the Fee Transmittal (PTO/SB/17) is enclosed for this purpose.

Date: 12/12/03

Respectfully submitted,

BLAKELY, SOLOLOFF, TAYLOR & ZAFMAN LLP


Eric S. Hyman, Reg. No. 30,139

12400 Wilshire Boulevard, 7th Floor
Los Angeles, CA 90025
Telephone: (310) 207-3800

Send To: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450

Based on PTO/SB/08B (08-03) as modified by Blakely, Solokoff, Taylor & Zafman (wlr) 08/11/2003.
Send To: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450

Information Disclosure Statement

New U.S. Patent Application for
QUADRATURE MODULATION TRANSMITTER
Our Ref. No.: P03EB025/US/js

Reference No.:

- (1) US Patent No. 6,507,625
- (2) US Patent No. 6,240,142

(3) A VLSI Architecture for a High-Speed All-Digital Quadrature Modulator and Demodulator for Digital Radio Applications
(IEEE JOURNAL ON SELECTED AREAS IN COMMUNICATIONS, VOL. 8, NO. 8, OCTOBER 1990, PAGES 1512-1519)

(4) A Digital Quadrature Modulator With On-Chip D/A Converter
(IEEE JOURNAL OF SOLID-STATE CIRCUITS, VOL. 38, NO. 10, OCTOBER 2003, PAGES 1635-1642)